

PATENT NUMBER

**U.S. UTILITY Patent Application**

**O.I.P.E.**

PATENT DATE

**SCANNED**

## Q.A

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/615705	F	257	355	2811	Nadav

## APPLICANTS

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**TITLE**

Semiconductor integrated circuit

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
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